



Welcome to the SPS Seasonal School on Signal Processing and Communication Systems for 5G. The jointly, internationally, organized by IEEE Vizag Bay Section Communications Society & SPS Joint Chapter, Vizag, India, IEEE Long Island Section SPS Chapter, New York, USA, and IEEE Finland SP/CAS Chapter, Finland presents the following Technical Lecture:

LDPC based Advanced Error Correction Coding and 5G

Tuesday September 14th 2021 at 8AM-12PM Pacific

Abstract:

This lecture gives an overview of HLS tools and evaluation of HLS flow vs. manually optimized traditional RTL design flow for LDPC decoders. Then this talk gives an overview of LDPC implementations for 5G-NR on various platforms such as GPUs, ASICs and FPGAs.

Technical Lecturer:



Dr. Francisco García-Herrero received the B.Sc. degree in telecommunication engineering from the Escuela Politécnica Superior de Gandia, Spain, in 2008, and the M.S. and Ph.D. degrees in electrical engineering from the Universitat Politècnica de València, Spain, in 2010 and 2013, respectively. Francisco Garcia-Herrero has worked as a Lecturer and a Researcher at several universities, including the European University Miguel de Cervantes and the Universitat Politècnica de València.

Francisco Garcia-Herrero is currently an Associate Professor and a Researcher with the Universidad Antonio de Nebrija. His research interests include hardware and algorithmic optimizations of error-control decoders and fault-tolerance electronics in communication and storage systems.

Logistics:

When: The 14th of September 2021 @ 8am—12pm PST

Where: Virtual Meeting

Registration Fees:

Free for all registered participants

Registration: <https://attend.ieee.org/sps-ss/>

Technical Lecture Coordinators:

Dr. Murthy, Chair of IEEE Vizag Bay Section Chapter (tsnmurthy@ieee.org)

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